

Introduction to Logic Design

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Features:

- ❑ Clear and lucid writing style
- ❑ Extensive set of examples integrated into the text and at the end of each chapter in sections of solved problems
- ❑ Hardware and simulation laboratory exercises tied closely to the text material
- ❑ Use of the Karnaugh Map to help students understand the principles of switching algebra
- ❑ Coupling of gate implementation with the algebra
- ❑ Thorough discussion of minimization of switching functions using Karnaugh maps, including 6-variable maps and multiple output problems
- ❑ An algorithmic minimization approach using iterated consensus
- ❑ Design using standard medium scale integrated circuit packages and programmable logic devices.
- ❑ Complete coverage of analysis and design of synchronous sequential systems
- ❑ Derivation of state tables from word problems
- ❑ State reduction and state assignment using partitions